

Docket No. AMDP796US

H1930



DFW

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re **PATENT** application of:

Applicant: James Pan et al.

Application No.: 10/634,042

For: A METHOD OF FABRICATING A DUAL-LEVEL STACKED FLASH  
MEMORY CELL WITH A MOSFET STORAGE TRANSISTOR  
(As amended)

Filing Date: August 4, 2003

Examiner: Dung A. Le

Art Unit: 2818

**REPLY TO OFFICE ACTION DATED MARCH 31, 2005**

Mail Stop Amendment  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Favorable reconsideration of the above-identified application is respectfully  
requested in view of the following remarks.